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**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2133**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,491
Filed: October 12, 2001
Inventor(s):
Michael C. Dorsey

Title: ASIC BIST EMPLOYING
STORED INDICATIONS
OF COMPLETION

Examiner: Trimmings, J.
Group/Art Unit: 2133
Atty. Dkt. No: 5681-55400

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter
Printed Name
Signature
Date 2/2/05

**AMENDMENT; RESPONSE TO OFFICE ACTION OF
December 3, 2004**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of December 3, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

OK to enter. *ppf*
8/18/05